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Substitute for form 1449/PTG

APR 26 2006

## **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

REMADE by sheets as necessary

Sheet 1 of 2

*Complete if Known*

Application Number	10/532,456
Filing Date	04/22/2005
First Named Inventor	GUNTER, Liberty L. et al.
Art Unit	2823
Examiner Name	COLEMAN, William D.
Attorney Docket Number	20030213-US

## U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No.*	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US-3,966,501	06-29-1976	Nomura et al.	
		US-6,410,396 B1	06-25-2002	Casady et al.	
		US-6,533,874 B1	03-18-2003	Vaudo et al.	
		US-6,545,340 B1	04-08-2003	Higgs et al.	
		US-2002/0117685 A1	08-29-2002	Mochizuki et al.	
		US-			
		IUS-			
		US-			
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## FOREIGN PATENT DOCUMENTS

Examiner Signature	/Quovaunda Jefferson/	Date Considered	02/25/2008
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Substitute for form 1449/PTO				Complete if Known	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				<b>Application Number</b>	10/532,456
				<b>Filing Date</b>	04/22/2005
				<b>First Named Inventor</b>	GUNTER, Liberty L. et al.
				<b>Art Unit</b>	2823
				<b>Examiner Name</b>	COLEMAN, William D.
(Use as many sheets as necessary)				<b>Attorney Docket Number</b>	20030213-US
Sheet	2	of	2		

## NON-PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		PCT International Search Report dated April 7, 2005 of International Application No. PCT/US04/32276 filed October 1, 2004.	
		NISHIZAWA, JUN-ICHI et al., "The 2.45 GHz 36 W CW Si Recessed Gate Type SIT with High Gain and High Voltage Operation", IEEE Transactions On Electronic Devices, February 2000, pp. 482-487, Vol. 47, No. 2.	
		HENNING, J.P. et al., "A Novel Self-Aligned Fabrication Process for Microwave Static Induction Transistors in Silicon Carbide", IEEE Electron Device Letters, December 2000, pp. 578-580, Vol. 21, No. 12.	
		BUNEA, GABRIELA E. et al., "Modeling of a GaN Based Static Induction Transistor", Dept. of Physics, Electrical and Computer Engineering, Boston University, pp. 1-6, Boston, MA, (NO Date)	
		CAMARCHIA, VITTORIO et al. "Physics-Based Modeling of Submicron GaN Permeable Base Transistors", IEEE Electron Device Letters, June 2002, pp. 303-305, Vol. 23, No. 6.	
		MISRA M. et al. "Investigation of vertical transport in n-GaN films grown by molecular beam epitaxy using Schottky barrier diodes", Applied Physics Letters, Feb. 21, 2000, pp. 1045-1047, Vol. 76, No. 8, American Institute of Physics.	
		BOZLER, CARL O. et al. "Fabrication and Numerical Simulation of the Permeable Base Transistor", IEEE, 1980, pp. 1-14.	

Examiner Signature	/Quovaunda Jefferson/	Date Considered	02/25/2008
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